INTERSECTION: Meridian & Main

Group Assignment: **NONE**
Field Master Assignment: **NONE**
System Reference Number: **7**

N/S Street Name: **Meridian**
E/W Street Name: **Main**

Last Database Change: **6/7/2013 10:27**

Change Record					
Change	By	Date	Change	By	Date

Notes:

Manual Plan
0 = Automatic
1-9 = Plan 1-9
14 = Free
15 = Flash

Manual Offset
0 = Automatic
1 = Offset A
2 = Offset B
3 = Offset C

Drop Number	3	<C/0+0+0>
Zone Number	1	<C/0+0+1>
Area Number	1	<C/0+0+2>
Area Address	8	<C/0+0+3>
QuicNet Channel	COM1:	(QuicNet)

Communication Addresses

Manual Plan		<C/0+A+1>
Manual Offset		<C/0+B+1>

Manual Selection

Flash Start	0	<F/1+0+E>
Red Revert	5.0	<F/1+0+F>
All Red Start	5.0	<F/1+C+0>

Start / Revert Times

Exclusive Walk	0	<F/1+0+0>
Exclusive FDW	0	<F/1+0+1>
All Red Clear	0.0	<F/1+0+2>

Exclusive Ped Phase
(Outputs specified in Assignable Outputs at E/127+A+E & F)

Row	Column Numbers ---->	Phase 1	2	3	4	5	6	7	8
	Phase Names ---->								
0	Ped Walk	0	7	0	7	0	0	0	7
1	Ped FDW	0	7	0	7	0	0	0	7
2	Min Green	0	4	0	4	0	0	0	4
3	Type 3 Disconnect	0	0	0	0	0	0	0	0
4	Added per Vehicle	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
5	Veh Extension	0.0	3.0	0.0	3.0	0.0	0.0	0.0	3.0
6	Max Gap	0.0	3.0	0.0	3.0	0.0	0.0	0.0	3.0
7	Min Gap	0.0	3.0	0.0	3.0	0.0	0.0	0.0	3.0
8	Max Limit	0	50	0	50	0	0	0	50
9	Max Limit 2	0	50	0	50	0	0	0	50
A	Adv. / Delay Walk	0	0	0	0	0	0	0	0
B	PE Min Ped FDW	0	0	0	0	0	0	0	0
C	Cond Serv Check	0	0	0	0	0	0	0	0
D	Reduce Every	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
E	Yellow Change	0.0	3.0	0.0	3.0	0.0	0.0	0.0	3.0
F	Red Clear	0.0	1.0	0.0	1.0	0.0	0.0	0.0	1.0

Phase Timing - Bank 1 <C+0+F=1>

	9	A	B	C	D
	---	---	---	---	---
Phase 1	0	0	0	0	0.0
Phase 2	20	0	0	0	0.0
Phase 3	0	0	0	0	0.0
Phase 4	20	0	0	0	0.0
Phase 5	0	0	0	0	0.0
Phase 6	20	0	0	0	0.0
Phase 7	0	0	0	0	0.0
Phase 8	20	0	0	0	0.0
Max Initial					
Alternate Walk					
Alternate FDW					
Alternate Initial					
Alternate Extension					

Alternate Timing <C+0+F=1>

	E
RR-1 Delay	0
RR-1 Clear	0
EV-A Delay	0
EV-A Clear	5
EV-B Delay	0
EV-B Clear	5
EV-C Delay	0
EV-C Clear	5
EV-D Delay	0
EV-D Clear	0
RR-2 Delay	0
RR-2 Clear	21
View EV Delay	---
View EV Clear	---
View RR Delay	---
View RR Clear	---

Preempt Timing

Row		F
0	Permit	2 4 8
1	Red Lock	
2	Yellow Lock	
3	Min Recall	
4	Ped Recall	
5	*View Set Peds*	-----
6	Rest In Walk	
7	Red Rest	
8	Dual Entry	4 8
9	Max Recall	
A	Soft Recall	
B	Max 2	
C	Cond. Service	
D	Man Cntrl Calls	
E	Yellow Start	
F	First Phases	2

Phase Functions <C+0+F=1>

Row	Column Numbers ---->	Overlap 1	2	3	4	5	6	7	8
	Overlap Name ---->								
0	Load Switch Number	0	0	0	0	0	0	0	0
1	Veh Set 1 - Phases								
2	Veh Set 2 - Phases								
3	Veh Set 3 - Phases								
4	Neg Veh Phases								
5	Neg Ped Phases								
6	Green Omit Phases								
7	Green Clear Omit Phs.								
8									
9									
A									
B									
C									
D	Green Clear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
E	Yellow Change	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0
F	Red Clear	0.0	0.0	0.0	0.0	0.0	0.0	0.0	0.0

Overlap Assignments <C+0+E=29>

Extra 1 Flags
1 = TBC Type 1
2 = NEMA Ext. Coord
3 = Auto Daylight Savings
4 = Solid FDW on EV
5 = Extended Status
6 = International Ped
7 = Flash - Clear Outputs
8 = Split Ring

Extra 2 Flags
1 = AWB During Initial
2 = LMU Installed
3 = Disable Min Walk
4 = QuicNet/4 System
5 = Ignore P/P on EV
6 =
7 = Reserved
8 =

Row		C
0	EV-A	0
1	EV-B	0
2	EV-C	0
3	EV-D	0
4	RR-1 *	---
5	RR-2 *	---
6	SE-1	0
7	SE-2	0
8		
9		
A		
B		
C		
D		
E		
F		

Preempt Priority <C+0+E=125>
(* RR-1 is always Highest, and RR-2 is always Second Highest)

Row	Column Numbers ---->	E
0	Exclusive Phases	
1	RR-1 Clear Phases	
2	RR-2 Clear Phases	2
3	RR-2 Limited Service	4 8
4	Prot / Perm Phases	
5	Flash to PE Circuits	
6	Flash Entry Phases	
7	Disable Yellow Range	
8	Disable Ovp Yel Range	
9	Overlap Yellow Flash	
A	EV-A Phases	2
B	EV-B Phases	4
C	EV-C Phases	
D	EV-D Phases	8
E	Extra 1 Config. Bits	1 34
F	IC Select (Interconnect)	2

Configuration <C+0+E=125>

	F
Ext. Permit 1 Phases	
Ext. Permit 2 Phases	
Exclusive Ped Assign	
Preempt Non-Lock	
Ped for 2P Output	2
Ped for 6P Output	2
Ped for 4P Output	4
Ped for 8P Output	8
Yellow Flash Phases	
Low Priority A Phases	
Low Priority B Phases	
Low Priority C Phases	
Low Priority D Phases	
Restricted Phases	
Extra 2 Config. Bits	

Configuration <C+0+E=125>

	F
Fast Green Flash Phase	
Green Flash Phases	
Flashing Walk Phases	
Guaranteed Passage	
Simultaneous Gap Term	12345678
Sequential Timing	
Advance Walk Phases	
Delay Walk Phases	
External Recall	
Start-up Overlap Green	
Max Extension	
Inhibit Ped Reservice	
Semi-Actuated	
Start-up Overlap Yellow	
Start-up Vehicle Calls	12345678
Start-up Ped Calls	12345678

Specials <C+0+F=2>

Flash to PE & PE Non-Lock
1 = EV A 5 = RR 1
2 = EV B 6 = RR 2
3 = EV C 7 = SE 1
4 = EV D 8 = SE 2

IC Select Flags
1 =
2 = Modem
3 = 7-Wire Slave
4 = Flash / Free
5 =
6 = Simplex Master
7 = 7-Wire Master
8 = Offset Interrupter

	2
Phase 1	0
Phase 2	18
Phase 3	0
Phase 4	18
Phase 5	0
Phase 6	0
Phase 7	0
Phase 8	18

Coordination Transition Minimums <C+0+C=5>

Coord Extra
1 = Programmed WALK Time for Sync Phases
2 = Always Terminate Sync Phase Peds

Row	Column Numbers ---->	Plan 1	2	3	4	5	6	7	8	9	Row
	Plan Name ---->										
0	Cycle Length	45	60	75	90	0	110	100	0	0	0
1	Phase 1 - ForceOff	0	0	0	0	0	0	0	0	0	1
2	Phase 2 - ForceOff	0	0	0	0	0	0	0	0	0	2
3	Phase 3 - ForceOff	0	0	0	0	0	0	0	0	0	3
4	Phase 4 - ForceOff	22	28	34	42	0	40	40	0	0	4
5	Phase 5 - ForceOff	0	0	0	0	0	0	0	0	0	5
6	Phase 6 - ForceOff	0	0	0	0	0	0	0	0	0	6
7	Phase 7 - ForceOff	0	0	0	0	0	0	0	0	0	7
8	Phase 8 - ForceOff	22	28	34	42	0	40	40	0	0	8
9	Ring Offset	0	0	0	0	0	0	0	0	0	9
A	Offset 1	36	36	36	37	0	41	35	0	0	A
B	Offset 2	0	0	0	0	0	0	0	0	0	B
C	Offset 3	0	0	0	0	0	0	0	0	0	C
D	Perm 1 - End	12	12	12	12	0	12	12	0	0	D
E	Hold Release	255	255	255	255	0	255	255	0	0	E
F	Zone Offset	0	0	0	0	0	0	0	0	0	F

Coordination - Bank 1 <C+0+C=1>

Row		E	Row
			0
1	Plan 1 - Sync	2	1
2	Plan 2 - Sync	2	2
3	Plan 3 - Sync	2	3
4	Plan 4 - Sync	2	4
5	Plan 5 - Sync	2	5
6	Plan 6 - Sync	2	6
7	Plan 7 - Sync	2	7
8	Plan 8 - Sync	2	8
9	Plan 9 - Sync	2	9
A	NEMA Sync		A
B	NEMA Hold		B
C			C
D			D
E	Coord Extra	2	E
F			F

Sync Phases <C+0+C=1>

Row		1	2	3	4	5	6	7	8	9	Row
0	Ped Adjustment	0	0	0	0	0	0	0	0	0	0
1	Perm 2 - Start	0	0	0	0	0	0	0	0	0	1
2	Perm 2 - End	0	0	0	0	0	0	0	0	0	2
3	Perm 3 - Start	0	0	0	0	0	0	0	0	0	3
4	Perm 3 - End	0	0	0	0	0	0	0	0	0	4
5	Reservice Time	0	0	0	0	0	0	0	0	0	5
6	Reservice Phases										6
7											7
8	Pretimed Phases	2 4 8	2 4 8	2 4 8	2 4 8	2 4 8	2 4 8	2 4 8	2 4 8	2 4 8	8
9	Max Recall										9
A	Perm 1 Veh Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	A
B	Perm 1 Ped Phase	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	12345678	B
C	Perm 2 Veh Phase										C
D	Perm 2 Ped Phase										D
E	Perm 3 Veh Phase										E
F	Perm 3 Ped Phase										F

Coordination - Bank 2 <C+0+C=2>

Row		F	Row
0	Free Lag	2 4 6 8	0
1	Plan 1 - Lag	2 4 6 8	1
2	Plan 2 - Lag	2 4 6 8	2
3	Plan 3 - Lag	2 4 6 8	3
4	Plan 4 - Lag	2 4 6 8	4
5	Plan 5 - Lag	2 4 6 8	5
6	Plan 6 - Lag	2 4 6 8	6
7	Plan 7 - Lag	2 4 6 8	7
8	Plan 8 - Lag	2 4 6 8	8
9	Plan 9 - Lag	2 4 6 8	9
A	External Lag		A
B			B
C			C
D			D
E			E
F			F

Lag Phases <C+0+C=1>

Row	Column 9		Column A		Column B		Column C		Column D		Column E		Column F		Row
0	Spec. Funct. 1	0	NOT-3	0	Max 2	0	Pretimed	0	Set Monday	0	Dial 2 (7-Wire)	0	Sim Term	0	0
1	Spec. Funct. 2	0	NOT-4	0	System Det 1	0	Plan 1	0	Ext. Perm 1	0	Dial 3 (7-Wire)	0	EV-A	71	1
2	Spec. Funct. 3	0	OR-4 (a)	0	System Det 2	0	Plan 2	0	Ext. Perm 2	0	Offset 1 (7-Wire)	0	EV-B	72	2
3	Spec. Funct. 4	0	OR-4 (b)	0	System Det 3	0	Plan 3	0	Reserved	0	Offset 2 (7-Wire)	0	EV-C	73	3
4	NAND-3 (a)	0	OR-5 (a)	0	System Det 4	0	Plan 4	0	Set Clock	0	Offset 3 (7-Wire)	0	EV-D	74	4
5	NAND-3 (b)	0	OR-5 (b)	0	System Det 5	0	Plan 5	0	Stop Time	82	Free (7-Wire)	0	RR-1	51	5
6	NAND-4 (a)	0	OR-6 (a)	0	System Det 6	0	Plan 6	0	Flash Sense	81	Flash (7-Wire)	0	RR-2	52	6
7	NAND-4 (b)	0	OR-6 (b)	0	System Det 7	0	Plan 7	0	Manual Enable	0	Excl. Ped Omit	0	Spec. Event 1	0	7
8	OR-7 (a)	0	Fig 3 Diamond	0	System Det 8	0	Plan 8	0	Man. Advance	0	NOT-1	0	Spec. Event 2	0	8
9	OR-7 (b)	0	Fig 4 Diamond	0	Max Inhibit (nema)	0	Plan 9	0	External Alarm	0	NOT-2	0	External Lag	0	9
A	OR-7 (c)	0	AND-4 (a)	0	Force A (nema)	0	DELAY-A	0	Phase Bank 2	0	OR-1 (a)	0	AND-1 (a)	0	A
B	OR-7 (d)	0	AND-4 (b)	0	Force B (nema)	0	DELAY-B	0	Phase Bank 3	0	OR-1 (b)	0	AND-1 (b)	0	B
C	OR-8 (a)	0	NAND-1 (a)	0	C.N.A. (nema)	0	DELAY-C	0	Overlap Set 2	0	OR-2 (a)	0	AND-2 (a)	0	C
D	OR-8 (b)	0	NAND-1 (b)	0	Hold (nema)	0	DELAY-D	0	Overlap Set 3	0	OR-2 (b)	0	AND-2 (b)	0	D
E	OR-8 (c)	0	NAND-2 (a)	0	Max Recall	0	DELAY-E	0	Detector Set 2	0	OR-3 (a)	0	AND-3 (a)	0	E
F	OR-8 (d)	0	NAND-2 (b)	0	Min Recall	0	DELAY-F	0	Detector Set 3	0	OR-3 (b)	0	AND-3 (b)	0	F

Assignable Inputs <C+0+E=126>

Row	Column 9		Column A		Column B		Column C		Column D		Column E		Column F		Row
0	Phase ON - 1	0	Preempt Fail	0	Flasher 0	0	Free	0	NOT-1	0	TOD Out 1	0	Dial 2 (7-Wire)	0	0
1	Phase ON - 2	0	Sp Evnt Out 1	0	Flasher 1	0	Plan 1	0	OR-1	0	TOD Out 2	0	Dial 3 (7-Wire)	0	1
2	Phase ON - 3	0	Sp Evnt Out 2	0	Fast Flasher	0	Plan 2	0	OR-2	0	TOD Out 3	0	Offset 1 (7-Wire)	0	2
3	Phase ON - 4	0	Sp Evnt Out 3	0	Fig 3 Diamond	0	Plan 3	0	OR-3	0	TOD Out 4	0	Offset 2 (7-Wire)	0	3
4	Phase ON - 5	0	Sp Evnt Out 4	0	Fig 4 Diamond	0	Plan 4	0	AND-1	0	TOD Out 5	0	Offset 3 (7-Wire)	0	4
5	Phase ON - 6	0	Sp Evnt Out 5	0			Plan 5	0	AND-2	0	TOD Out 6	0	Free (7-Wire)	0	5
6	Phase ON - 7	0	Sp Evnt Out 6	0			Plan 6	0	AND-3	0	TOD Out 7	0	Flash (7-Wire)	0	6
7	Phase ON - 8	0	Sp Evnt Out 7	0			Plan 7	0	NOT-2	0	TOD Out 8	0	Preempt	0	7
8	Ph. Check - 1	0	Sp Evnt Out 8	0	NOT-3	0	Plan 8	0	EV-A	35	Adv. Warn - 1	0	Low Priority A	0	8
9	Ph. Check - 2	0			NOT-4	0	Plan 9	0	EV-B	37	Adv. Warn - 2	0	Low Priority B	0	9
A	Ph. Check - 3	0	Detector Fail	0	OR-4	0	Spec. Funct. 3	0	EV-C	36	DELAY-A	0	Low Priority C	0	A
B	Ph. Check - 4	0	Spec. Funct. 1	0	OR-5	0	Spec. Funct. 4	0	EV-D	38	DELAY-B	0	Low Priority D	0	B
C	Ph. Check - 5	0	Spec. Funct. 2	0	OR-6	0	NAND-3	0	RR-1	0	DELAY-C	0			C
D	Ph. Check - 6	0	Central Control	0	AND-4	0	NAND-4	0	RR-2	0	DELAY-D	0			D
E	Ph. Check - 7	0	Excl. Ped DW	0	NAND-1	0	OR-7	0	Spec. Event 1	0	DELAY-E	0			E
F	Ph. Check - 8	0	Excl. Ped WK	0	NAND-2	0	OR-8	0	Spec. Event 2	0	DELAY-F	0			F

Assignable Outputs <C+0+E=127>

Row	Phase Names ---->	Phase 1	2	3	4	5	6	7	8
0	Ped Walk	0	7	0	7	0	7	0	7
1	Ped FDW	0	15	0	15	0	15	0	15
2	Min Green	4	7	4	4	4	7	4	4
3	Type 3 Disconnect	0	20	0	20	0	20	0	20
4	Added per Vehicle	0.0	2.0	0.0	2.0	0.0	2.0	0.0	2.0
5	Veh Extension	2.0	4.0	2.0	2.5	2.0	4.0	2.0	2.5
6	Max Gap	3.0	6.0	3.0	3.0	3.0	6.0	3.0	3.0
7	Min Gap	0.5	2.0	0.5	1.5	0.5	2.0	0.5	1.5
8	Max Limit	20	30	20	25	20	30	20	25
9	Max Limit 2	30	50	30	40	30	50	30	40
A	Adv. / Delay Walk	0	0	0	0	0	0	0	0
B	PE Min Ped FDW	7	7	7	7	7	7	7	7
C	Cond Serv Check	10	10	10	10	10	10	10	10
D	Reduce Every	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
E	Yellow Change	3.0	4.0	3.0	3.0	3.0	4.0	3.0	3.0
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 2 <C+0+F=2>

	9	A	B	C	D
	---	---	---	---	---
Phase 1	0	0	0	0	0.0
Phase 2	0	0	0	0	0.0
Phase 3	0	0	0	0	0.0
Phase 4	0	0	0	0	0.0
Phase 5	0	0	0	0	0.0
Phase 6	0	0	0	0	0.0
Phase 7	0	0	0	0	0.0
Phase 8	0	0	0	0	0.0

Max Initial
Alternate Walk
Alternate FDW
Alternate Initial
Alternate Extension

Alternate Timing

Transition Type
0.X = Shortway
1.X = Lengthen
X.1 thru X.4 = Number of cycles when lengthing

Transition Type	0.1	<C/5+1+9>

TBC Transition

Lag Hold Phases	_____	<C/5+1+A>

Coordinated Lag Hold Phases

Sync Output Time	0.0	<C/5+1+C>

7-Wire Master

Daylight Savings Date
If set to all zeros, standard dates will be used.

Begin Month	3	<C/5+2+A>
Begin Week	2	<C/5+2+B>
End Month	11	<C/5+2+C>
End Week	1	<C/5+2+D>

Daylight Savings Time

Time B4 Yellow	0.0	<F/1+C+E>
Phase Number	0	<F/1+C+F>

Advance Warning Beacon - Sign 1

Time B4 Yellow	0.0	<F/1+D+E>
Phase Number	0	<F/1+D+F>

Advance Warning Beacon - Sign 2

Long Failure	0.7	<F/1+0+6>
Short Failure	0.7	<F/1+0+7>

Power Cycle Correction (Default = 0.7)

Row		1	2	3	4	5	6	7	8
0	Ped Walk	0	7	0	7	0	7	0	7
1	Ped FDW	0	15	0	15	0	15	0	15
2	Min Green	4	7	4	4	4	7	4	4
3	Type 3 Disconnect	0	20	0	20	0	20	0	20
4	Added per Vehicle	0.0	2.0	0.0	2.0	0.0	2.0	0.0	2.0
5	Veh Extension	2.0	4.0	2.0	2.5	2.0	4.0	2.0	2.5
6	Max Gap	3.0	6.0	3.0	3.0	3.0	6.0	3.0	3.0
7	Min Gap	0.5	2.0	0.5	1.5	0.5	2.0	0.5	1.5
8	Max Limit	20	30	20	25	20	30	20	25
9	Max Limit 2	30	50	30	40	30	50	30	40
A	Adv. / Delay Walk	0	0	0	0	0	0	0	0
B	PE Min Ped FDW	7	7	7	7	7	7	7	7
C	Cond Serv Check	10	10	10	10	10	10	10	10
D	Reduce Every	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0
E	Yellow Change	3.0	4.0	3.0	3.0	3.0	4.0	3.0	3.0
F	Red Clear	1.0	1.0	1.0	1.0	1.0	1.0	1.0	1.0

Phase Timing - Bank 3 <C+0+F=3>

	9	A	B	C	D
	---	---	---	---	---
Phase 1	0	0	0	0	0.0
Phase 2	0	0	0	0	0.0
Phase 3	0	0	0	0	0.0
Phase 4	0	0	0	0	0.0
Phase 5	0	0	0	0	0.0
Phase 6	0	0	0	0	0.0
Phase 7	0	0	0	0	0.0
Phase 8	0	0	0	0	0.0

Max Initial
Alternate Walk
Alternate FDW
Alternate Initial
Alternate Extension

Alternate Timing

Column Numbers ---->	0	1	2	3	1	3
Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0		39	45_7_	_2_____	123___8	0.0	0.0
1		40	45_7_	____6__	123___8	0.0	0.0
2		41	45_7_	___4___	123___8	0.0	0.0
3		42	45_7_	_____8	123___8	0.0	0.0
4		43	45_7_	_2_____	123___8	0.0	0.0
5		44	45_7_	____6__	123___8	0.0	0.0
6		45	45_7_	___4___	123___8	0.0	0.0
7		46	45_7_	_____8	123___8	0.0	0.0
8		47	67_____	_2_____	123___8	0.0	0.0
9		48	67_____	____6__	123___8	0.0	0.0
A		49	67_____	___4___	123___8	0.0	0.0
B		50	67_____	_____8	123___8	0.0	0.0
C		55	45_7_	____5__	123___8	0.0	0.0
D		56	45_7_	1_____	123___8	0.0	0.0
E		57	45_7_	_____7_	123___8	0.0	0.0
F		58	45_7_	___3___	123___8	0.0	0.0

Detector Assignments <C+0+E=126>

	4	5	6	7	2	4
Row	Detector Name	C1 Pin Number	Attributes	Phase(s)	Assign	Delay	Carry-over
0		59	45_7_	____5__	123___8	0.0	0.0
1		60	45_7_	1_____	123___8	0.0	0.0
2		61	45_7_	_____7_	123___8	0.0	0.0
3		62	45_7_	___3___	123___8	0.0	0.0
4		63	45_7_	_2_____	123___8	0.0	0.0
5		64	45_7_	____6__	123___8	0.0	0.0
6		65	45_7_	___4___	123___8	0.0	0.0
7		66	45_7_	_____8	123___8	0.0	0.0
8		67	_2_____	_2_____	123___8	0.0	0.0
9		68	_2_____	____6__	123___8	0.0	0.0
A		69	_2_____	___4___	123___8	0.0	0.0
B		70	_2_____	_____8	123___8	0.0	0.0
C		76	45_7_	_2_____	123___8	0.0	0.0
D		77	45_7_	____6__	123___8	0.0	0.0
E		78	45_7_	___4___	123___8	0.0	0.0
F		79	45_7_	_____8	123___8	0.0	0.0

<C+0+D=0>

Detector Attributes
1 = Full Time Delay
2 = Ped Call
3 =
4 = Count
5 = Extension
6 = Type 3
7 = Calling
8 = Alternate

Det. Assignments
1 = Det. Set 1
2 = Det. Set 2
3 = Det. Set 3
4 =
5 =
6 = Failure - Min Recall
7 = Failure - Max Recall
8 = Report on Failure

Column Numbers ---->	Ped / Phase / Overlap 1	2	3	4	5	6	7	8	Row
Walk	0	0	0	0	0	0	0	0	0
Don't Walk	0	0	0	0	0	0	0	0	1
Phase Green	0	0	0	0	0	0	0	0	2
Phase Yellow	0	0	0	0	0	0	0	0	3
Phase Red	0	0	0	0	0	0	0	0	4
Overlap Green	0	0	0	0	0	0	0	0	5
Overlap Yellow	0	0	0	0	0	0	0	0	6
Overlap Red	0	0	0	0	0	0	0	0	7

Redirect Phase Outputs <C+0+E=127>

Cabinet Type	0	<E/125+D+0>

Enable Redirection
(Enable Redirection = 30)

Max OFF (minutes)	20	<D/0+0+1>
Max ON (minutes)	7	<D/0+0+2>

Detector Failure Monitor

	D	Row
Output Port 1		1
Output Port 2		2
Output Port 3		3
Output Port 4		4
Output Port 5		5
Output Port 6		6
Output Port 7		7

Dimming <C+0+E=125>

	D
Number of Digits	0
1 st Digit	0
2 ed Digit	0
3 ed Digit	0
4 th Digit	0
5 th Digit	0
6 th Digit	0
7 th Digit	0
8 th Digit	0
9 th Digit	0
10 th Digit	0
11 th Digit	0
12 th Digit	0
13 th Digit	0
14 th Digit	0
15 th Digit	0

<C+0+C=5>

Dial-Back Telephone Number

Disable Alarms
1 = Stop Time
2 = Flash Sense
3 = Keyboard Entry
4 = Manual Plan
5 = Police Control
6 = External Alarm
7 = Detector Failure
8 =

	B	Row
DELAY-A	0	A
DELAY-B	0	B
DELAY-C	0	C
DELAY-D	0	D
DELAY-E	0	E
DELAY-F	0	F

Delay Logic Times
<C+0+D=0> (seconds)

Omit Alarm		<C/5+F+0>

Disable Alarm Reporting

Time	0	<C/5+C+0>

Redial Time (minutes)
(View Redial Timer at E/2+D+6)

Row	Time	Plan	Offset	Day of Week
0	07 : 00	3	A	1234567
1	10 : 00	3	A	1
2	10 : 30	6	A	234567
3	17 : 30	7	A	234567
4	18 : 30	2	A	1234567
5	00 : 01	2	A	1234567
6	00 : 00	0	0	
7	00 : 00	0	0	
8	00 : 00	0	0	
9	00 : 00	0	0	
A	00 : 00	0	A	
B	00 : 00	0	A	
C	00 : 00	0	A	
D	00 : 00	0	A	
E	00 : 00	0	A	
F	00 : 00	0	A	

TOD Coordination <C+0+9=0.1>
(Bank 1)

Time	Funct	Day of Week	Column 4 Phases/Bits
00 : 00	E	1234567	78
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		

TOD Function <C+0+7=0.1> <C+0+E=27>

Day	Year	Month	Holiday Type
07	12	9	4
08	12	9	5
09	12	9	1
10	12	9	2
11	12	9	3
12	12	9	3
13	12	9	3
14	12	9	4
15	12	9	5
16	12	9	1
17	12	9	2
18	12	9	3
19	12	9	3
20	12	9	3
21	12	9	4
22	12	9	5

Holiday Dates <C+0+8=1.1>
(Bank 1)

Time	Plan	Offset	Holiday Type
07 : 20	6	A	23 5
09 : 00	6	A	12345
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
21 : 00	2	A	12345
00 : 01	2	A	12345
00 : 00	0	A	
14 : 15	7	A	6
18 : 30	2	A	6
20 : 15	7	A	6
00 : 00	4	A	6
02 : 00	3	A	6
00 : 00	0	A	

Holiday Events <C+0+9=1.1>
(Bank 1)

T.O.D. Functions
0 =
1 = Red Lock
2 = Yellow Lock
3 = Veh Min Recall
4 = Ped Recall
5 =
6 = Rest In Walk
7 = Red Rest
8 = Double Entry
9 = Veh Max Recall
A = Veh Soft Recall
B = Maximum 2
C = Conditional Service
D = Free Lag Phases
E = Bit 1 - Local Override
Bit 4 - Disable Detector OFF Monitor
Bit 5 - Disable Low Priority Preempt
Bit 7 - Detector Count Monitor
Bit 8 - Real Time Split Monitor
F = Output Bits 1 thru 8

Plan Select
1 thru 9 = Coordination Plan 1 thru 9
14 or E = Free
15 or F = Flash

Offset Select
A = Offset A
B = Offset B
C = Offset C

Month Select
1 = January
2 = February
3 = March
4 = April
5 = May
6 = June
7 = July
8 = August
9 = September
A = October
B = November
C = December

Row	Time	Plan	Offset	Day of Week
0	00 : 00	0	0	
1	00 : 00	0	0	
2	00 : 00	0	0	
3	00 : 00	0	0	
4	00 : 00	0	0	
5	00 : 00	0	0	
6	00 : 00	0	0	
7	00 : 00	0	0	
8	00 : 00	0	0	
9	00 : 00	0	0	
A	00 : 00	0	0	
B	00 : 00	0	0	
C	00 : 00	0	0	
D	00 : 00	0	0	
E	00 : 00	0	0	
F	00 : 00	0	0	

TOD Coordination <C+0+9=0.2>
(Bank 2)

Time	Funct	Holiday Type	Column 4 Phases/Bits
00 : 00	E	123456	1 78
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		
00 : 00	0		

Holiday TOD Function <C+0+7=0.2> <C+0+E=28>

Day	Year	Month	Holiday Type
23	12	9	1
19	06	6	6
20	06	6	6
21	06	6	6
22	06	6	6
00	00	0	
00	00	0	
00	00	0	
00	00	0	
00	00	0	
00	00	0	
00	00	0	
00	00	0	
00	00	0	
00	00	0	
00	00	0	

Holiday Dates <C+0+8=1.2>
(Bank 2)

Time	Plan	Offset	Holiday Type
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	
00 : 00	0	0	

Holiday Events <C+0+9=1.2>
(Bank 2)

Row	6 Clear	7 Time	8 Ped Call	9 Hold	A Advance	B Force Off	C Vehicle Call	D Permit Phases	E Ped Omit	F Output
0		0								
1		0								
2		0								
3		0								
4		0								
5		0								
6		0								
7		0								
8		0								
9		0								
A		0								
B		0								
C		0								
D		0								
E		0								
F		0								

Special Event Schedule -- Table 1 <C+0+E=27>

Notes:

0 <E/27+5+F>
Limited Service Interval

Row	6 Clear	7 Time	8 Ped Call	9 Hold	A Advance	B Force Off	C Vehicle Call	D Permit Phases	E Ped Omit	F Output
0		0								
1		0								
2		0								
3		0								
4		0								
5		0								
6		0								
7		0								
8		0								
9		0								
A	2	30		2		4 8	2	2 4 8		
B		0								
C		0								
D		0								
E		0								
F		0								

Special Event Schedule -- Table 2 <C+0+E=28>

Notes:

8 <E/28+5+F>
Limited Service Interval

Min Time (seconds) | 0 | <F/1+0+8>
Min Green Before PE Force Off

Max Time (minutes) | 255 | <F/1+0+9>
Max Preempt Time Before Failure

Min Time (seconds) | 0 | <F/1+0+A>
Min Time Between Same Preempts
(Does Not Apply To Railroad Preempt)

Low Pri. Channel | _____ | <E/125+C+8>
Disable Low Priority Channel

Low Priority
1 = Channel A
2 = Channel B
3 = Channel C
4 = Channel D

Delay Time (seconds) | 0 | <F/1+A+D>
Bus Delay

Max Time (seconds) | 0 | <F/1+A+E>
Max Early Green

Max Time (seconds) | 0 | <F/1+A+F>
Max Green Extension

Row	Time	Headway	Direction	Day of Week
0	00 : 00	0	0	_____
1	00 : 00	0	0	_____
2	00 : 00	0	0	_____
3	00 : 00	0	0	_____
4	00 : 00	0	0	_____
5	00 : 00	0	0	_____
6	00 : 00	0	0	_____
7	00 : 00	0	0	_____
8	00 : 00	0	0	_____
9	00 : 00	0	0	_____
A	00 : 00	0	0	_____
B	00 : 00	0	0	_____
C	00 : 00	0	0	_____
D	00 : 00	0	0	_____
E	00 : 00	0	0	_____
F	00 : 00	0	0	_____

Headway <C+0+9=2.1>

Headway Time
(minutes)
1 thru 9 = 1 thru 9
A = 10
B = 11
C = 12
D = 13
E = 14
F = 15

Low Priority Preemption (Bus Priority)

Only available with *Program 233RV2.B* (and above)
Note: Also see "Time of Day Functions", Function E, Bit 5 (Disable Low Priority)